

```

    0 0 0 1 0 0 0000 0>>00      0 0 0 1 1 0 0000 0>>00
[ 0 0 { } 0 0 0 0 0 }              [ 0 0 0 04 0 / 0 0
[ 0 0 0 - 20 / 0 20 50
[ 0 00 ] 0 0 0 2 0 0 3 0 0 0 0 0
[ 0 00 ] 0 10 / 0 20 60
[ 0 00 ] 0 0 0 10 10 / 0 70 0 0 0 0
[ 0 0 0 0 0 0 0 8 0 / 0 2 0 6 0 0 0 0 0 0 0 0

```

[illegible]

Diagram illustrating the addition of two numbers using a 4-bit register. The register initially contains the value 7 (0111). The second number, 0 (0000), is added to it. The result is 7 (0111).

Diagram illustrating the execution of the 'L' instruction. The ALU contains the value 1. The instruction 'L' is being executed, and the result 3 is being stored in the register. The ALU output is 3, and the register value is 3.

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□□□□□□

[illegible]

0000 0000 0000 0000 0000 0000 0000 0000 0000 0000

C o p y r i g h t A t l 12 0R 0i 4g -h 2t 0s 0 B e e r v e d .

□ □ □ □ □

0 5 9 5 - 8 6 4 7 7 9  OFFLINE 1 3 5 9 9 1 1 3 9 9 7

0 5 0 1 1 0 8 2

51.8